

PTO/SB/08A (02-03)

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Substitute for form 1449/PTO

**SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete If Known

Application Number	10/749,103
Filing Date	December 30, 2003
First Named Inventor	Rafael Reif
Art Unit	2891
Examiner Name	David A. Zameke
Attorney Docket Number	MIT-136BUS

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Examiner Signature	/David Zarneke/	Date Considered	07/20/2007
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 808. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinda Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

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OCT 27 2005

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)

Complete if Known

Sheet 1 of 1 Application Number 10/749,103
Filing Date December 30, 2003
First Named Inventor Rafael Relf
Art Unit 2891
Examiner Name David A. Zarneke
Attorney Docket Number MJT-136BUS

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/D.Z./		US- 5,504,376	04/02/1996	Sugahara et al.	
		US- 4,939,568	07/03/1990	Kato et al.	
		US- 5,902,118	05/11/1999	Hubner	
		US- 5,904,562	05/18/1999	Nulman	
		US- 5,998,808	12/07/1999	Matsushita	
		US- 6,525,415	02/25/2003	Koyanagi et al.	
		US- 6,600,173	07/29/2003	Tiwari	
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FOREIGN PATENT DOCUMENTS					
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Examiner Signature /David Zarneke/ Date Considered 07/20/2007

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT			
<i>(Use as many sheets as necessary)</i>			
Application Number		10/749,103	
Filing Date		December 30, 2003	
First Named Inventor		Rafael Reif	
Art Unit		2823	
Examiner Name		Not Yet Assigned David Zarneke	
Attorney Docket Number		MIT-136BUS	
Sheet	1	of	1

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Sheet 1 of 2

Application Number	10/749,103
Filing Date	December 30, 2003
First Named Inventor	Rafael Reif
Art Unit	2025 2891
Examiner Name	Not Yet Assigned Zarneke
Attorney Docket Number	MIT-136BUS

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
↓ /D.Z./		US- 5,236,118	08/17/1993	Bower et al.	
		US- 5,270,261	12/14/1993	Bertin et al.	
		US- 5,370,301	12/06/1994	Belcher et al.	
		US- 5,391,257	02/21/1995	Sullivan et al.	
		US- 5,563,084	10/08/1996	Ramm et al.	
		US- 5,669,545	09/23/1997	Pham et al.	
		US- 5,985,693	11/16/1999	Leedy	
		US- 6,027,958	02/22/2000	Vu et al.	
		US- 2002/0135075	08/26/2002	Leedy	
		US- 6,466,892	10/15/2002	Suga	
		US- 2004/0124538	07/01/2004	Reif et al.	
		US- 2004/0219765	11/04/2004	Reif et al.	
		US- 2004/0126994	07/01/2004	Reif et al.	
		US-			
		US-			

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/D.Z./		EP 1 041 624 A1	10/04/2000	Interuniversitair Microelektronica Centrum (IMC)	
/D.Z./		DE 100 47 963 A 1	03/29/2001	Sony Corp.	
/D.Z./		EP 1 432 032 A2	06/23/2004	Zel Serch - Interstar Energy Laboratory Co. LTD.	

Examiner Signature	/David Zarneke/	Date Considered	07/23/2007
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/749,103
Filing Date	December 30, 2003
First Named Inventor	Rafael Reif
Art Unit	2823 2891
Examiner Name	Not Yet Assigned Zarneke
Attorney Docket Number	MIT-136BUS

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS

[illegible]

Examiner Signature	/David Zarneke/	Date Considered	07/23/2007
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EXAMINER: Initial reference considered, whether or not citation is in conformance with MPEP §19. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique creation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.93. The information is required to obtain or maintain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEE: OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/749,103
				Filing Date	December 30, 2003
				First Named Inventor	Rafael Reif
				Art Unit	2820 2891
				Examiner Name	Not Yet Assigned Zarnke
				Attorney Docket Number	MIT-136BUS
Sheet	1	of	1		

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Examiner Signature	/David Zarneke/	Date Considered	07/23/2007
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Application Number	10/749,103
Filing Date	December 30, 2003
First Named Inventor	Rafael Reif
Art Unit	2833-2891
Examiner Name	Not Yet Assigned Zarneke
Attorney Docket Number	MIT-136BUS

Sheet	1	of	2
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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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Application Number	10/749,103
Filing Date	December 30, 2003
First Named Inventor	Rafael Reif
Art Unit	2623 2891
Examiner Name	Not Yet Assigned Zarneke
Attorney Docket Number	MIT-136BUS

Sheet 1 of 2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
/D.Z./ ↓		US- 5,706,578	01/13/1998	Hubner	
		US- 5,767,009	06/16/1998	Yoshida et al.	
		US- 5,825,080	10/20/1998	Imaoka et al.	
		US- 2002/0050635 A1	05/02/2002	Ogura et al.	
		US- 2002/00774670A1	06/20/2002	Suga	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ * Number * Kind Code ⁵ (if known)				
/D.Z./		2 645 681	04/07/1989	Thomson CSF		
/D.Z./		WO 02/09182 A1	01/31/2002	Christian Val		

Examiner Signature	/David Zarneke/	Date Considered	07/23/2007
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STATEMENT BY APPLICANT**

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Application Number		10/749,103		
	Filing Date		December 30, 2003		
	First Named Inventor		Rafael Reif		
	Art Unit		2823 2891		
	Examiner Name		Not Yet Assigned Zarneke		
Sheet	2	of	2	Attorney Docket Number	MIT-136BUS

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/D.Z./		Y. HAYASHI, Fabrication of Three Dimensional IC Using "Cumulatively Bonded IC" (CUBIC) Technology" Symposium on VLSI Technology, 1990, pages 95-96	
/D.Z./		PCT/US/03/41514 International Search Report June 17, 2004	

Examiner Signature	/David Zarneke/	Date Considered	07/23/2007
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

METHOD OF FORMING A MULTI-LAYER
SEMICONDUCTOR STRUCTURE INCORPORATING A
PROCESSING HANDLE MEMBER

Application Number: 10/749103



Confirmation Number: 3165

First Named Applicant: Rafael Reif

AU: 2891

Attorney Docket Number: MIT-136BUS

Examiner:

Search string: (4313126 or 4402761 or 4456888 or 4599704
or 4986861 or 5156997 or 5206186 or 5445994
or 5821138 or 5923087 or 6441478).pn.

Zarneke

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
/D.Z.	1	4313126	1982-01-26	Krumm et al.			
<input type="checkbox"/>	2	4402761	1983-09-06	Feist			
<input type="checkbox"/>	3	4456888	1984-01-26	Ayasli			
<input type="checkbox"/>	4	4599704	1986-07-08	Mazin			
<input type="checkbox"/>	5	4986861	1991-01-22	Nishida et al.			
<input type="checkbox"/>	6	5156997	1992-10-20	Kumar et al.			
<input type="checkbox"/>	7	5206186	1993-04-27	Neugebauer et al.			
<input type="checkbox"/>	8	5445994	1995-08-29	Gilton			
<input type="checkbox"/>	9	5821138	1998-10-13	Yamazaki et al.			
<input checked="" type="checkbox"/>	10	5923087	1999-07-13	Suzuki et al.			
/D.Z.	11	6441478	2002-08-27	Park			

Signature

Examiner Name	Date
/David Zarneke/	07/23/2007

PTO/SB/08B (02-03)

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Art Unit	2822 2891
Examiner Name	Not Yet Assigned Zarneke
Attorney Docket Number	MIT-138BUS

Sheet 1 of 1

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/D.Z./		CHRIS GAITHER, Intel Set To Unveil Advanced Communications Chip, The Boston Globe, October 15, 2002, Pages 1-3, Boston, Massachusetts	
/D.Z./		PETER CLARKE, IEDM Late Paper: IBM Shows How to Stack for 3D Circuitry, Silicon Strategies.Com, November 28, 2002, Pages 1-2, LONDON	
/D.Z./		TRU-SI TECHNOLOGIES, Through-Silicon Vias, Tru-Si Technologies Home Page, April 7, 2003, Page 1, Sunnyvale, California	
/D.Z./		MARK LAPEDUS, Tezzaron Devises Multi-Wafer Stack for 3-D Chips, Semiconductors EE Times, June 30, 2003, Pages 1-3, Naperville, Illinois	

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